



# NoBug Consulting

December 2021

# Who are we?

- 3 centers in **Romania**:
  - Bucharest, 49 engineers (1999)
  - Brasov, 29 engineers (2003)
  - Iasi, 22 engineers (2008)
- 2 centers in **Serbia**:
  - Belgrade, 17 engineers (2012)
  - Novi Sad, 1 Engineer (2019)
- 1 center in **Tunisia**:
  - Tunis, 8 engineers (2013)



## NoBug at a Glance

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NoBug is an engineering company providing verification and design services of all digital functionality of semiconductor products.

Established 22 years ago, the company has grown to ~120 engineers, and proudly counts 192 projects undertaken successfully for 36 clients.

### Areas of expertise

- Digital Design Verification for SoCs/ASICs/IPs
- VIPs Development for top EDA vendors
- EDA Software Development

### Ownership

- NoBug is an independent, profitable, debt free company owned by its founder.

## Vision, Mission, Strategy

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### Vision

As ASICs continue to increase in complexity, the need for robust comprehensive design verification continues to grow in importance. Recruiting, training, developing and sustaining multi-disciplinary verification teams is time consuming, expensive, and risky. With over 2 decades of experience in delivering design verification services, NoBug is continuously developing successful methods to grow its expertise, providing quality results for each and every project.

### Mission

Be the partner of choice for leading ASIC developers in developing bug-free microelectronic devices.

### Strategy

Establish strategic alliances designed to deliver comprehensive client-centric solutions

- Solving complex design and verification challenges
- Creating a sustainable competitive advantage for select, high quality client base
- Developing new strategic business opportunities

# Client Portfolio

- NoBug proudly counts 192 successful projects undertaken for 36 clients
- Consistently delights our customers by delivering high-quality results
- Reduce risks, steps and time to fully functional silicon

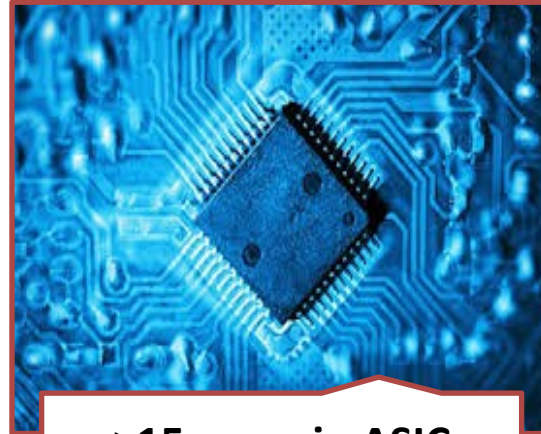




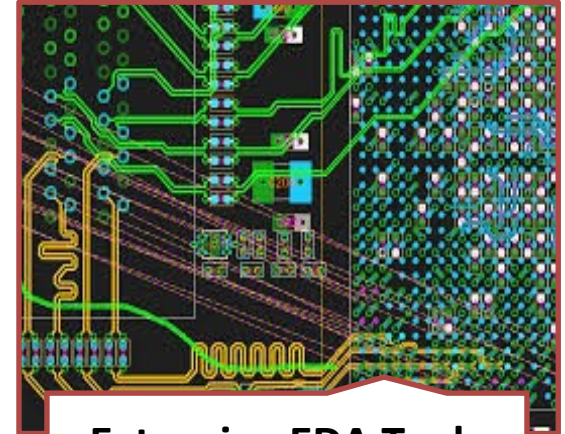
## Expertise



**>20 years in ASIC  
Verification**



**>15 years in ASIC  
Design**



**Extensive EDA Tools  
Experience**

**192  
Projects**

**320+  
Tapeouts**

**13,000+  
Bugs Caught**

**Satisfied  
Customers**



NOBUG<sup>®</sup>

